

**TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING  
REJECTION OVER "PRIOR" PATENT**

Docket Number (Optional)

060188-0725

In re Application of: M. OHTA, et al.

Application No.: 10/751,525

Filed: January 06, 2004

For: FUNCTIONAL BLOCK FOR INTEGRATED CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD FOR TESTING  
SEMICONDUCTOR INTEGRATED CIRCUIT, AND METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT

The owner\*, Matsushita Electric Industrial Co., Ltd., of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application which would extend beyond the expiration date of the full statutory term **prior patent** No. 6,708,301 as the term of said patent is defined in 35 U.S.C. 154 and 173, and as the term of said **prior patent** is presently shortened by any terminal disclaimer. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the **prior patent** are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 and 173 of the **prior patent**, "as the term of said **prior patent** is presently shortened by any terminal disclaimer," in the event that said **prior patent** later:

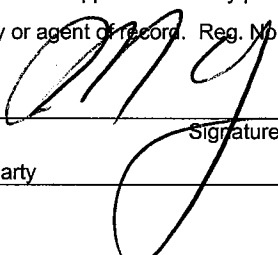
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2. ☒ The undersigned is an attorney or agent of record. Reg. No. 36,139


  
Signature

March 12, 2008

Date

Michael E. Fogarty

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202.756.8000

Telephone Number

- ☒ Terminal disclaimer fee under 37 CFR 1.20(d) is included ☒ Charge Deposit Account 500417 ☐ Check enclosed

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